

FIG. 1

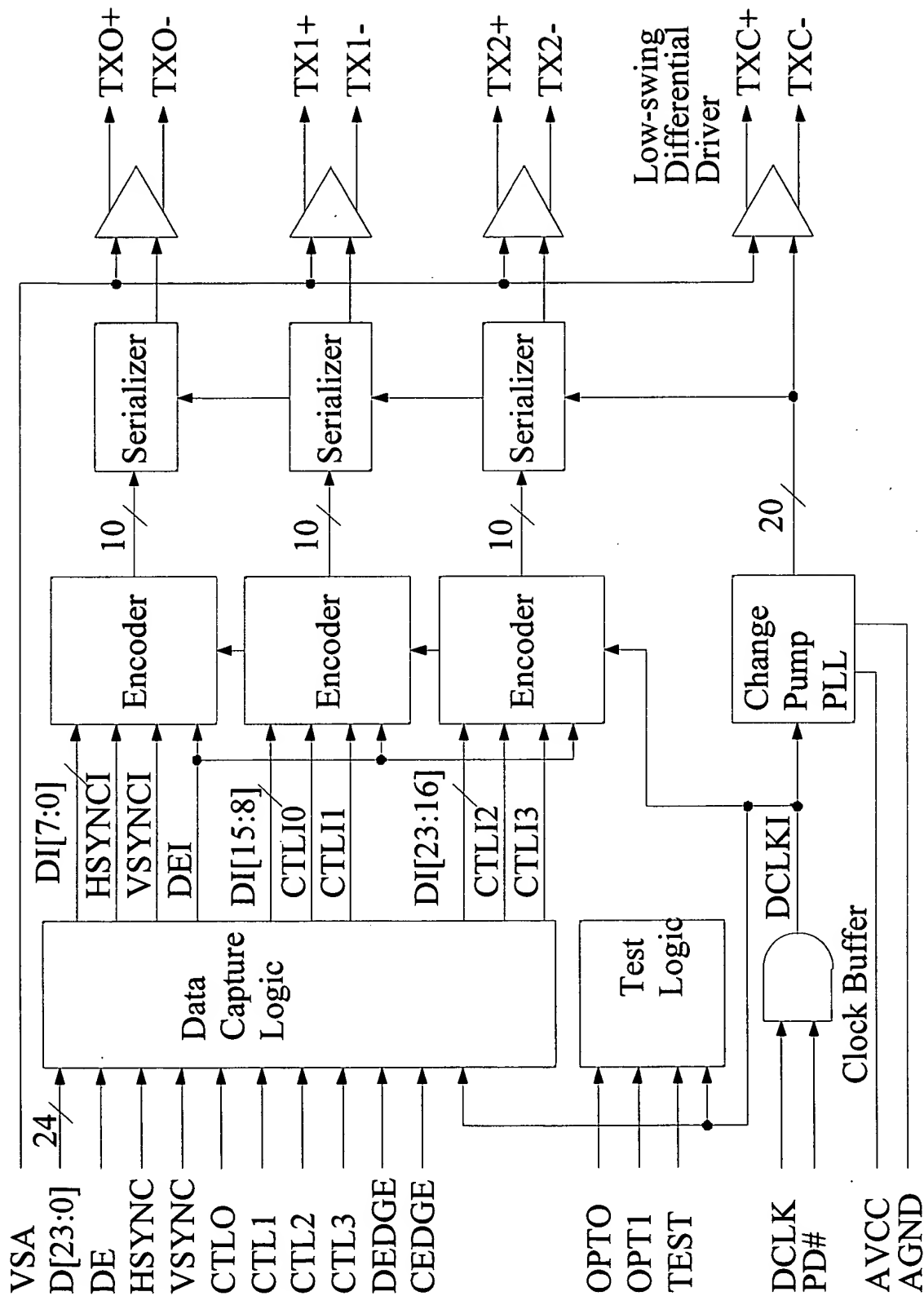


FIG. 2A

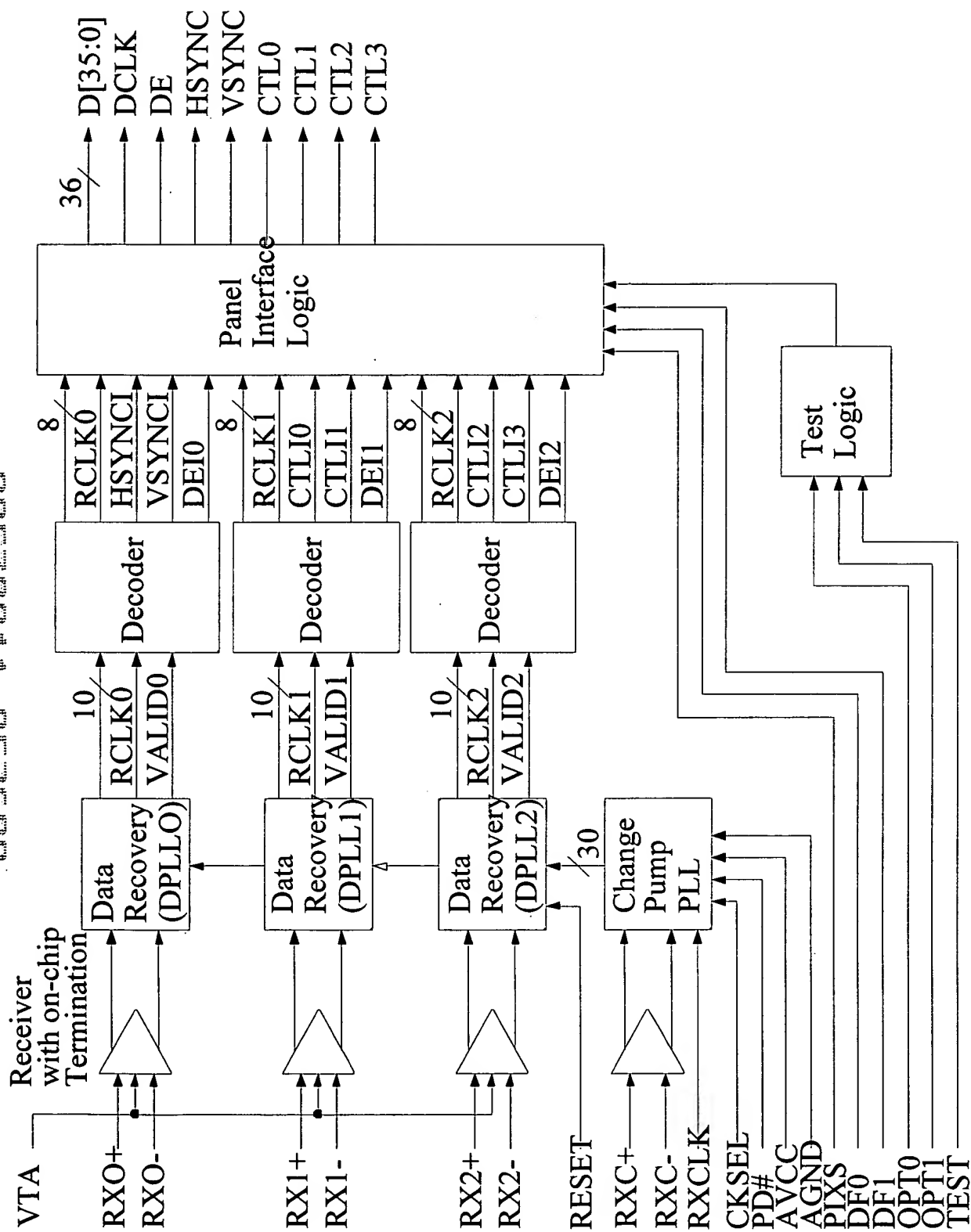


FIG. 2B

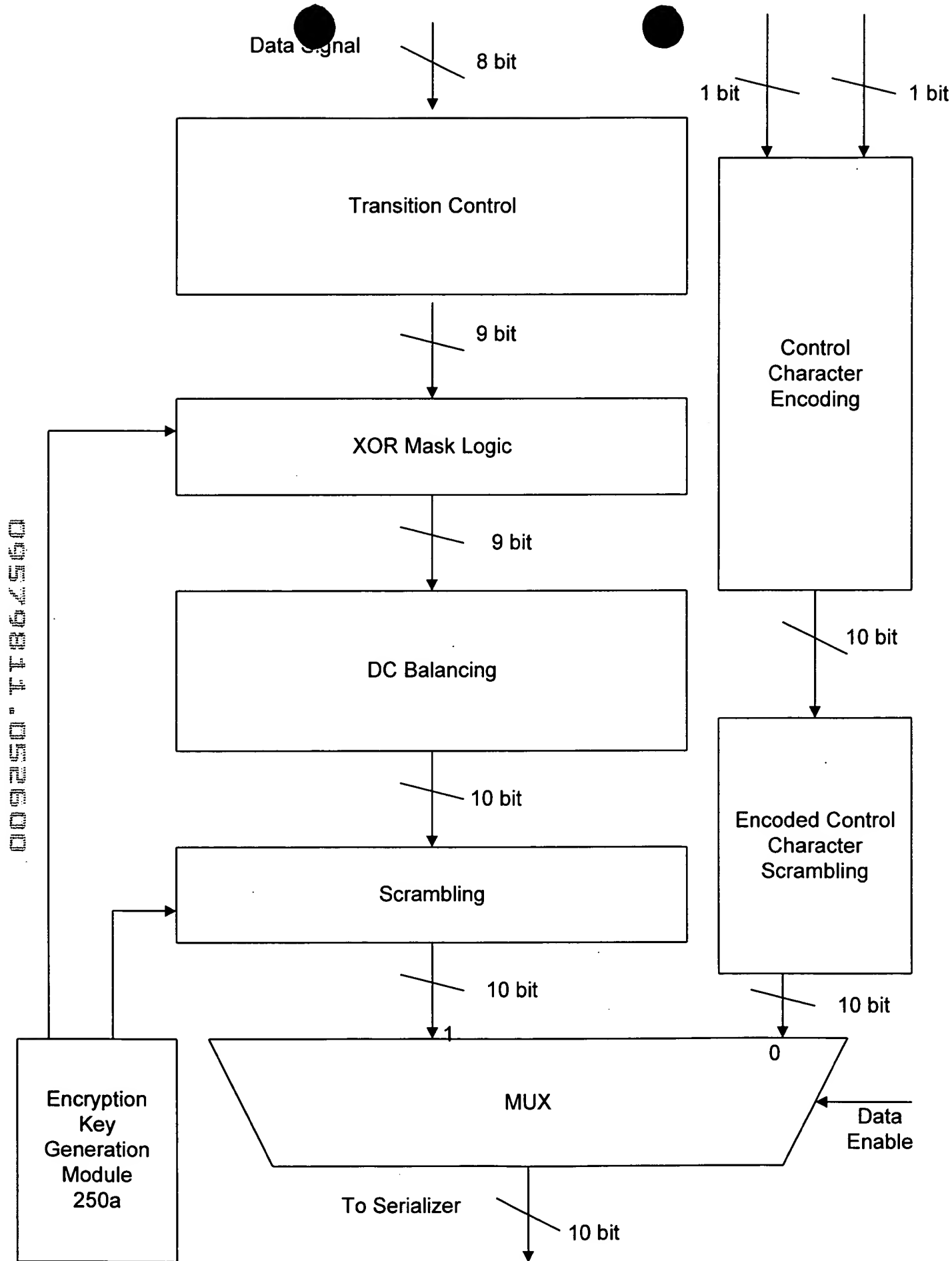


FIG. 3A

009250-11864560

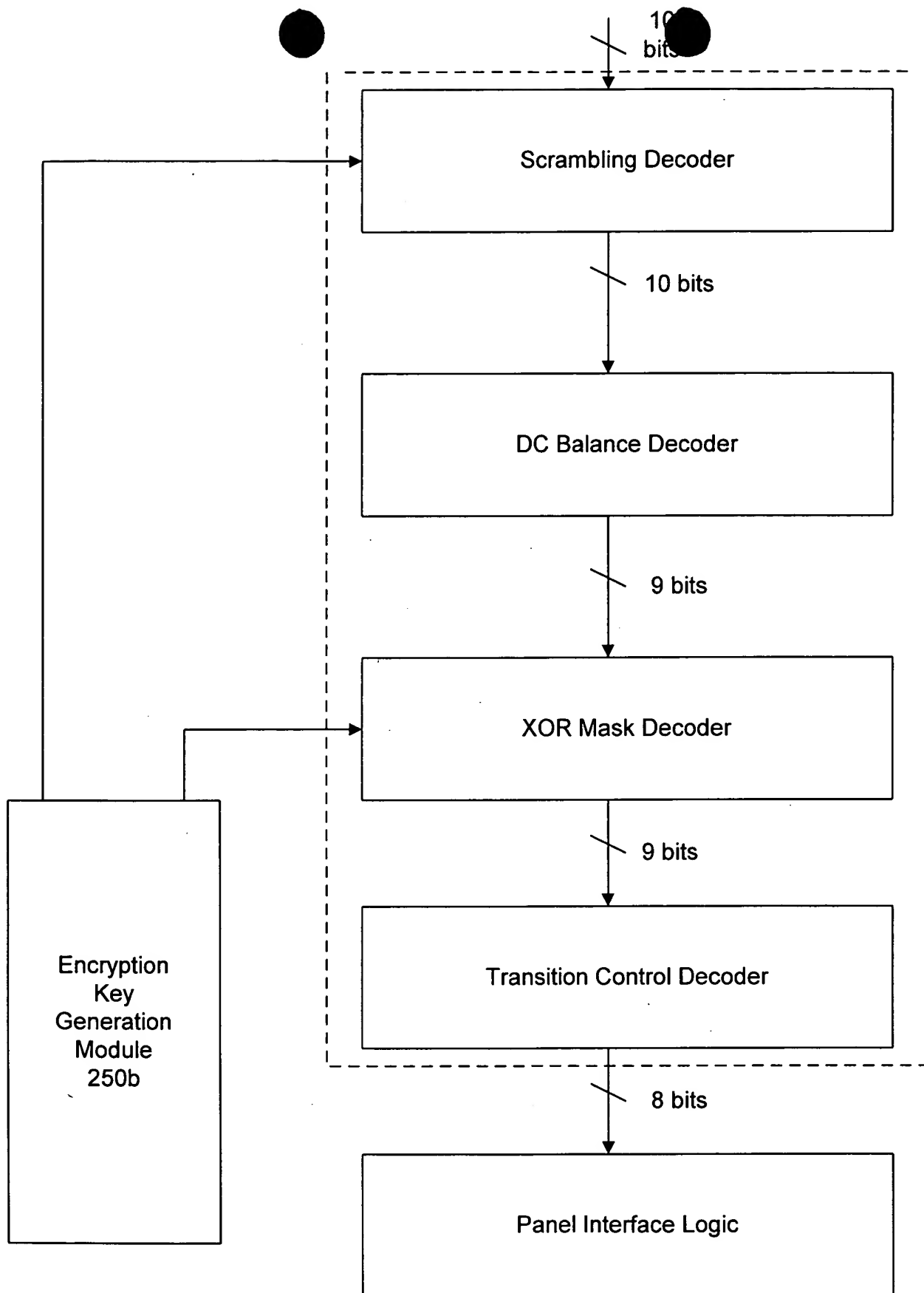


FIG. 3B

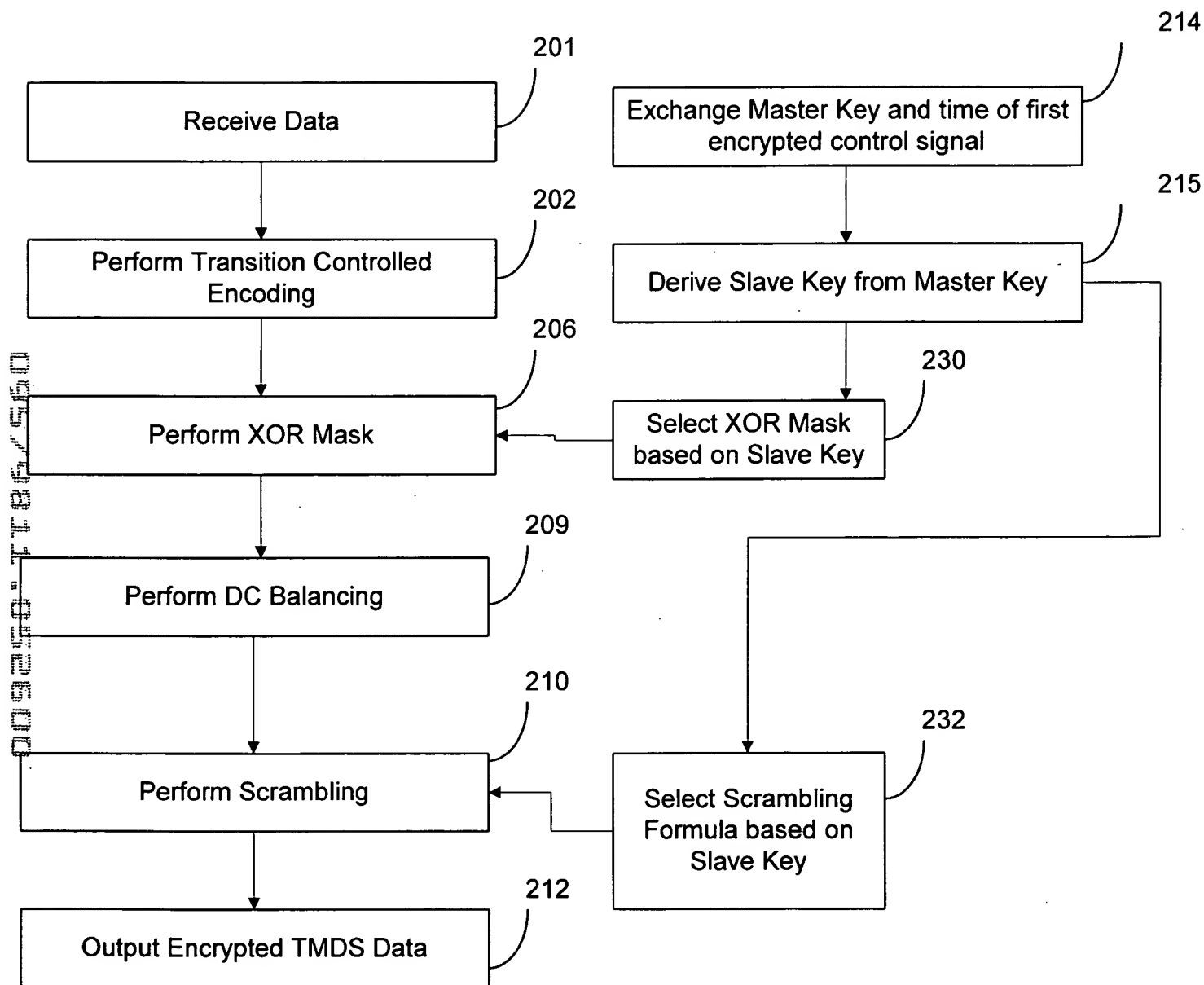


FIG. 4

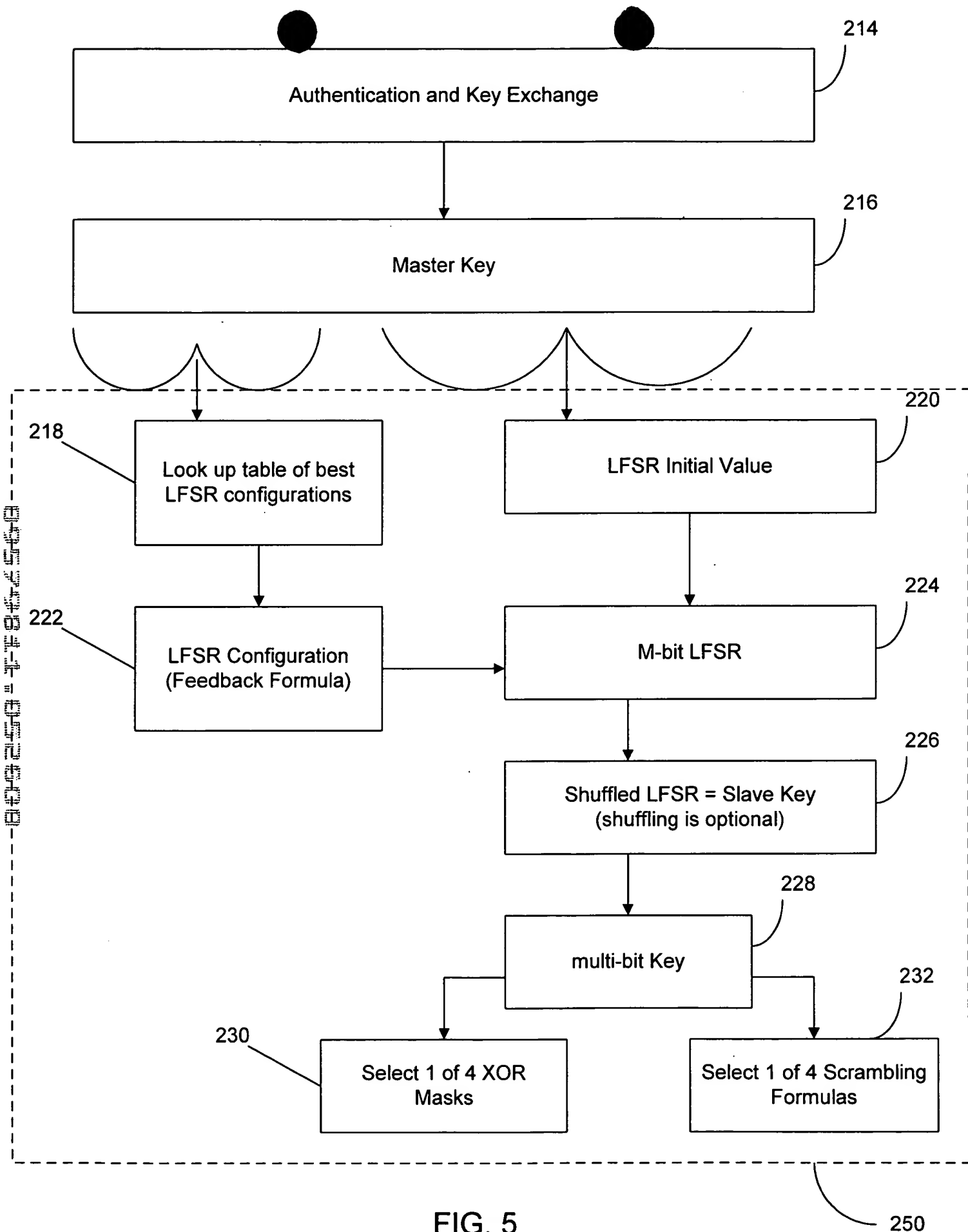


FIG. 5

XOR Mask
Logic

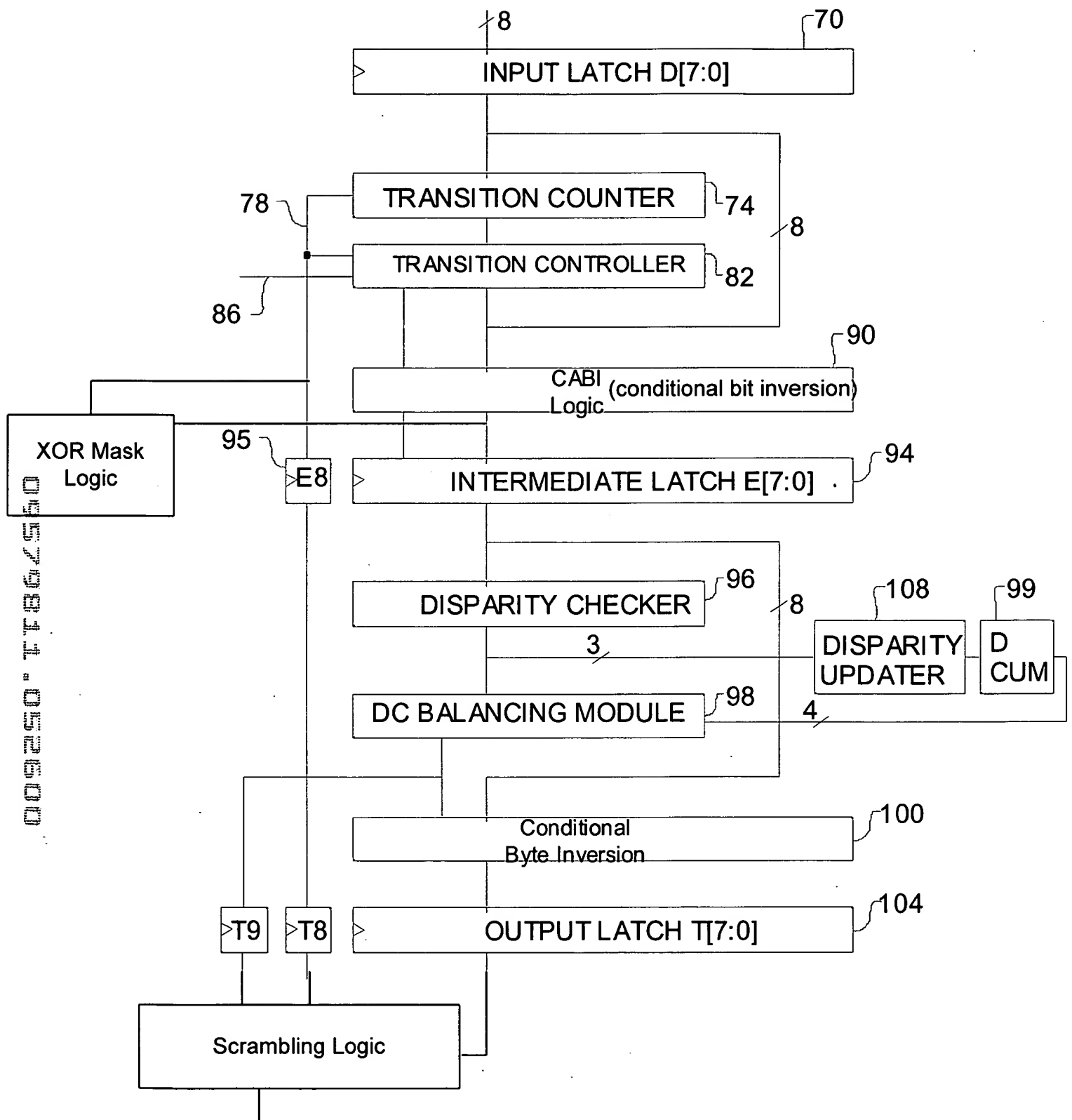


FIG. 6

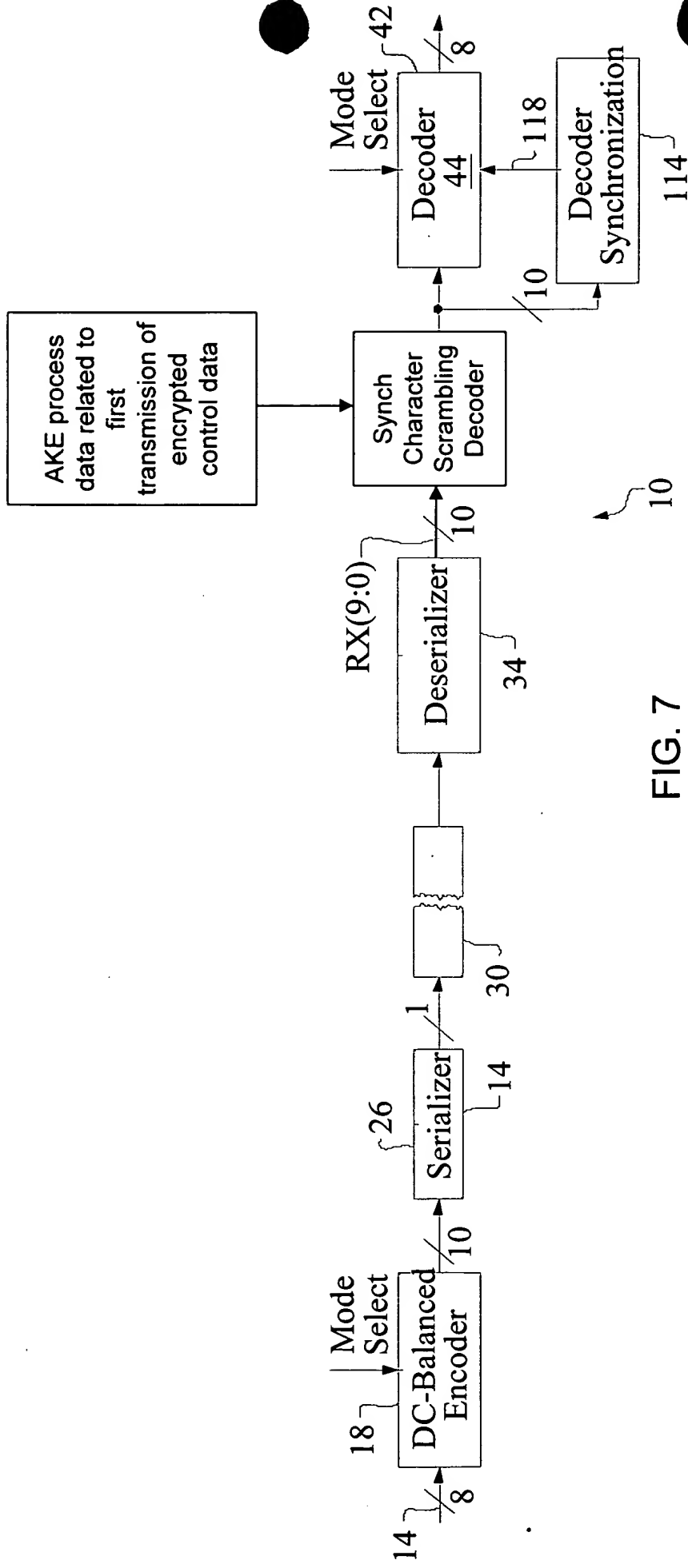


FIG. 7

009250-T22560

Timeline of
serial data
stream

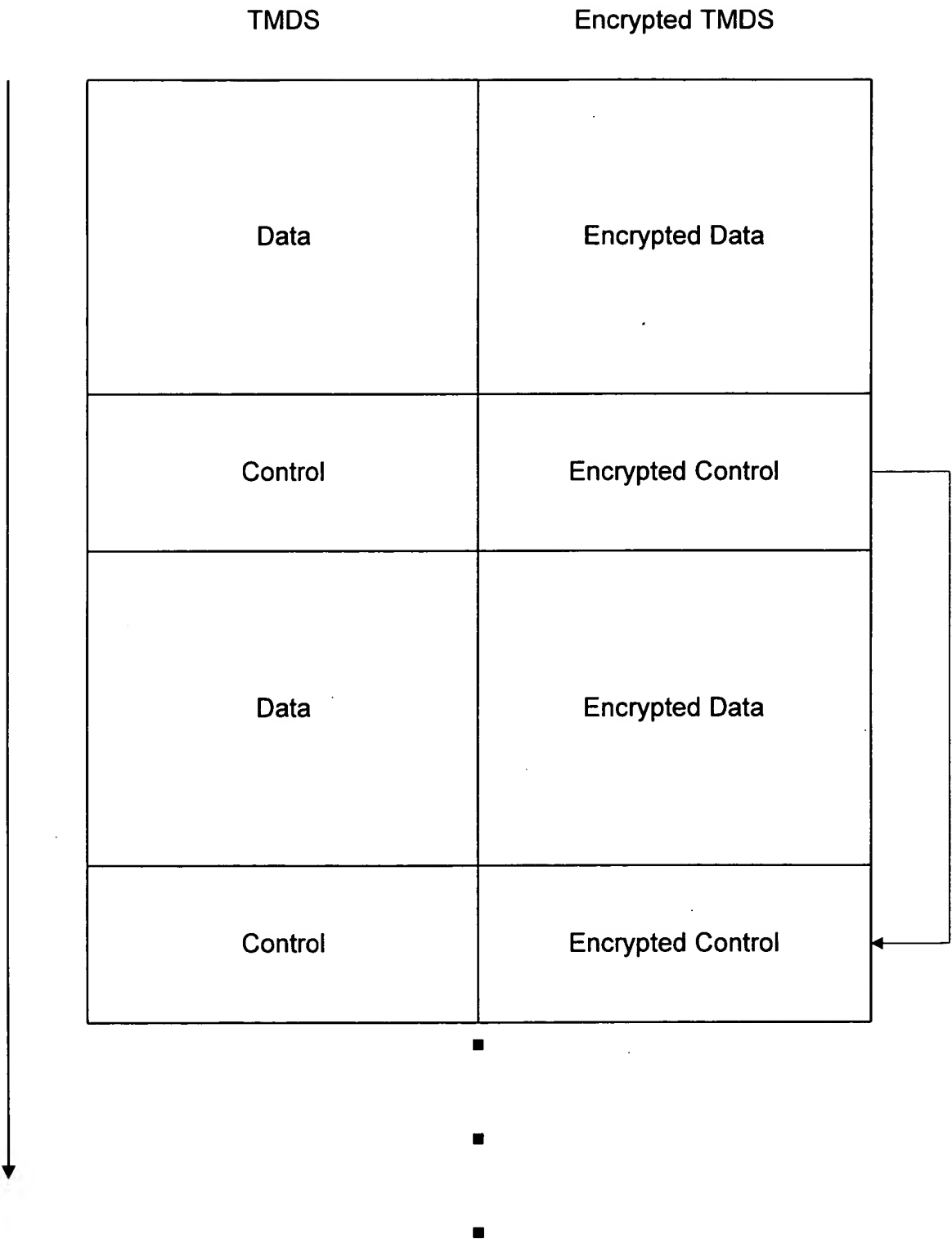


FIG. 8